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A

A

B

B

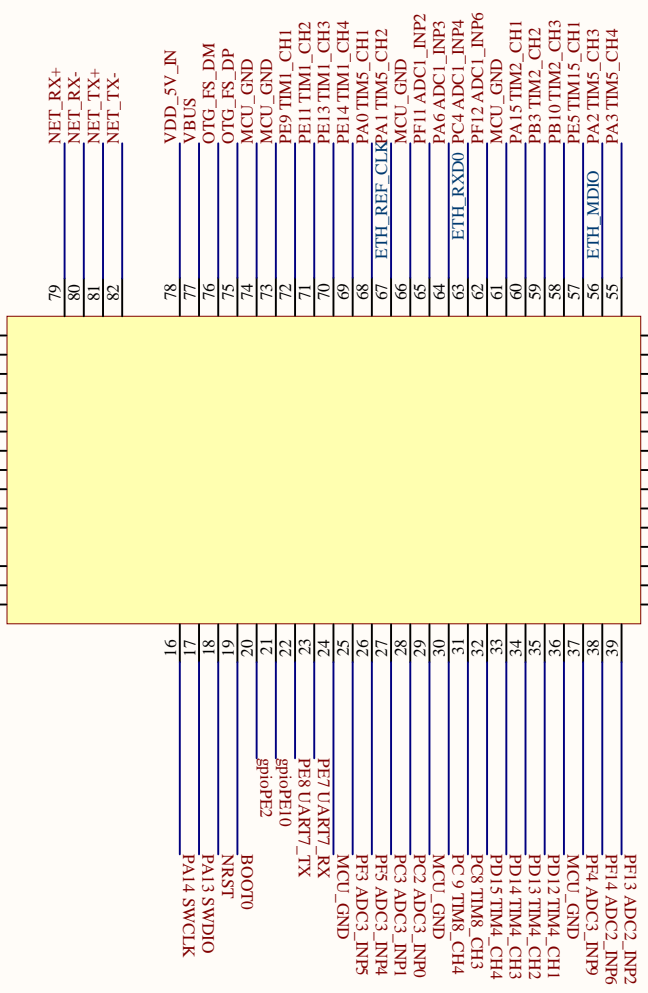
C

C

D

D

CAN1\_L 1  
 CAN1\_H 2  
 PE1 USART8\_TX 3  
 PE0 USART8\_RX 4  
 PB8 I2C1\_SCL 5  
 PB7 I2C1\_SDA 6  
 PC10 SPI3\_SCK 7  
 PC11 SPI3\_MISO 8  
 PC12 SPI3\_MOSI 9  
 PA10 SPI3\_CS 10  
 PB14 UBLEDOUT 11  
  
 VDD\_3V3 13  
 CAN2\_L 14  
 CAN2\_H 15



CANMOD\_FOOTPRINT  
 CM?  
 54 MCU\_GND  
 53 PC0 ADC1\_INP10  
 52 ETH\_MDC PC1 ADC1\_INP11  
 51 PA4 ADC1\_INP18  
 50 PA5 ADC1\_INP19  
 49 MCU\_GND  
 48 PB4 TIM3\_CH1  
 47 PE6 TIM15\_CH2  
 46 PB0 TIM3\_CH3  
 45 PB1 TIM3\_CH4  
 44 PC6 TIM8\_CH1  
 43 PC7 TIM8\_CH2  
 42 MCU\_GND  
 41 ETH\_CRSDMA7 ADC2\_INP7  
 40 ETH\_RXD1 PC5 ADC2\_INP8

Title		
Size	Number	Revision
A4		
Date:	9/18/2023	Sheet of
File:	C:\Current_DATA\...\Sheet1.SchDoc	Drawn By:

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